

APPROVAL SHEET

SR25_A, SR20_A

$\pm 1\%$, $\pm 5\%$

Thick Film Anti-Sulfur Chip Resistors

Size 2512, 2010

(Automotive & Anti-Sulfuration)

RoHS 2 Compliant with exemption 7C-I
Halogen free

FEATURE

1. High reliability and stability $\pm 1\%$
2. Sulfuration resistant by Oil 105°C soaked with 3.5% sulphur substance 500hrs
3. Automotive grade AEC Q-200 Qualified
4. 100% CCD inspection
5. RoHS 2 Compliant with exemption 7C-I and Halogen free products

APPLICATION

- Automotive application
- Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The extra protective metal film is added onto top side electrodes to protect termination from sulfuration. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

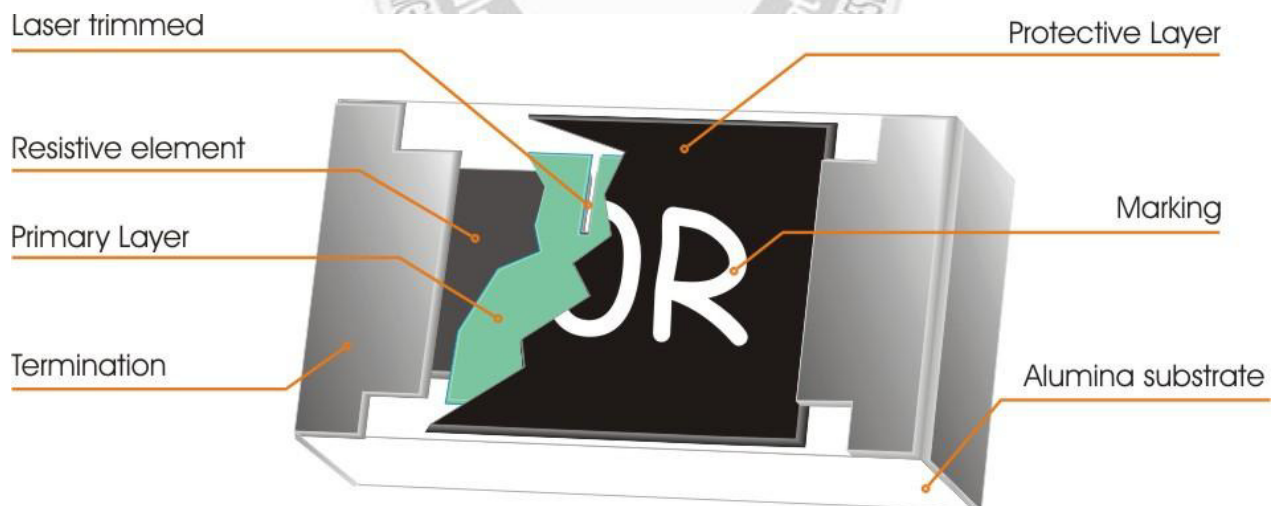


Fig 1. Construction of a Chip-R

QUICK REFERENCE DATA

Item	General Specification	
Series No.	SR25_A	SR20_A
Size code	2512(6432)	2010 (5025)
Resistance Range	1Ω~10MΩ	
Resistance Tolerance	± 1%, E96+E24 ± 5%, E24	
TCR (ppm/°C)	> 1MΩ ± 200 ppm/°C 10Ω ~ 1MΩ ± 100 ppm/°C < 10Ω ± 200 ppm/°C	
Max. dissipation @ T _{amb} =70°C	1 W	0.75 W
Max. Operation Voltage (DC or RMS)	250V	200V
Max. Overload Voltage (DC or RMS)	500V	400V
Operation temperature	-55 ~ +155°C	

Note :

1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

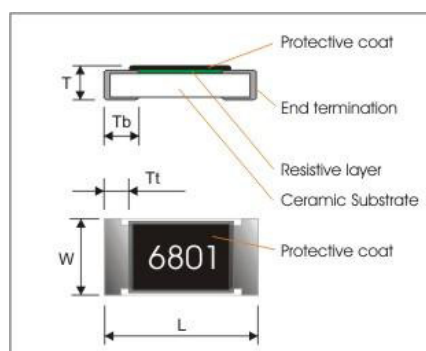
$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}} \text{ or Max. RCWV listed above, whichever is lower.}$$

For Jumper (0Ω) :

Item	General Specification	
Series No.	SR25_A	SR20_A
Size code	2512(6432)	2010 (5025)
Resistance Range	Max. 50mR	Max. 50mR
TCR (ppm/°C)	TCR is not applicable for Jumper product	
Max. dissipation @ T _{amb} =70°C	1 W	0.75 W
Rated Current	4.5 A	3.2 A
Peak Current	11 A	8 A
Operating Temperature	-55 ~ 155°C	-55 ~ 155°C

DIMENSIONS (unit : mm)

Series No.	SR25_A	SR20_A
L	6.40±0.20	5.00±0.20
W	3.20±0.20	2.50±0.20
T	0.60±0.10	0.55±0.10
Tb	0.90±0.25	0.60±0.25
Tt	0.85±0.25	0.65±0.25



CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

SR20	X	1003	F	T	L	A
Automotive code SR25 : 2512 SR20 : 2010	Type code X : ±1%, 10-1M ±5%, 1-10M W : ±1%, <10, >1M	Resistance code ±5%, E24: 2 significant digits followed by no. of zeros and a blank 220Ω = 221_ (" " means a blank) ±1%, E24+E96: 3 significant digits followed by no. of zeros 102Ω = 1020 37.4KΩ = 3742	Tolerance F : ±1% J : ±5% P : Jumper	Packaging code T : 7" Reeled taping Q : 10" Reeled taping G : 13" Reeled taping B : Bulk	Termination code L = Sn base (lead free)	Special code A = Anti-sulfur by Oil 105°C soaked with 3.5% sulphur 500hrs

* Anti-Sulfur test conditions: Oil 105°C soaked with 3.5% sulphur substance 500hrs, acceptance criteria: +/-1.0%.\

* 100% CCD visual inspection to guarantee visual quality.

MARKING

Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value ±5%, ±1% tolerance.

Size	±5%	±1%
2512, 2010	4-digits marking	4-digits marking

Example

RESISTANCE	Jumper (0Ω)	10Ω	12Ω	100Ω	6800Ω	47000Ω
4-digit marking	0000	10R0	12R0	1000	6801	4702

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E24& E96 series for resistors with a tolerance of $\pm 0.5\%$, $\pm 0.1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063"

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

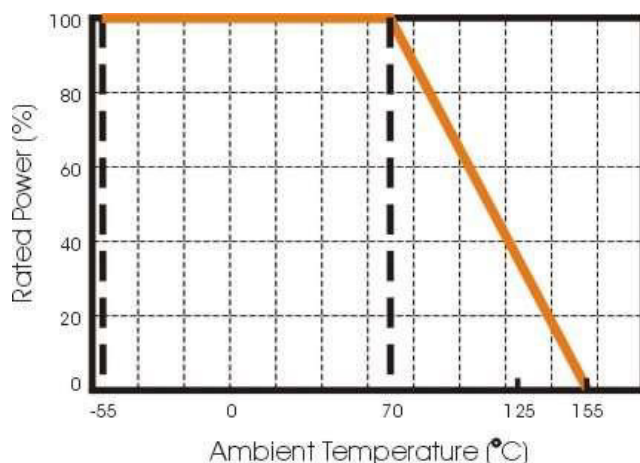


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

Storage and Handling Conditions:

1. Products are recommended to be used up within two years since operation date as ensured shelf life. Check solderability in case shelf life extension is needed.
2. To store products with following condition:
 - Temperature :5 to 40°C
 - Humidity :20 to 70% relative humidity
3. Caution:
 - a. Don't store products in a corrosive environment such as sulfide, chloride gas, or acid.
It may cause oxidation of electrode, which easily be resulted in poor soldering.
 - b. To store products on the shelf and avoid exposure to moisture.
 - c. Don't expose products to excessive shock, vibration, direct sunlight and so on.

SOLDERING CONDITION follows J-STD-020D

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds.

Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

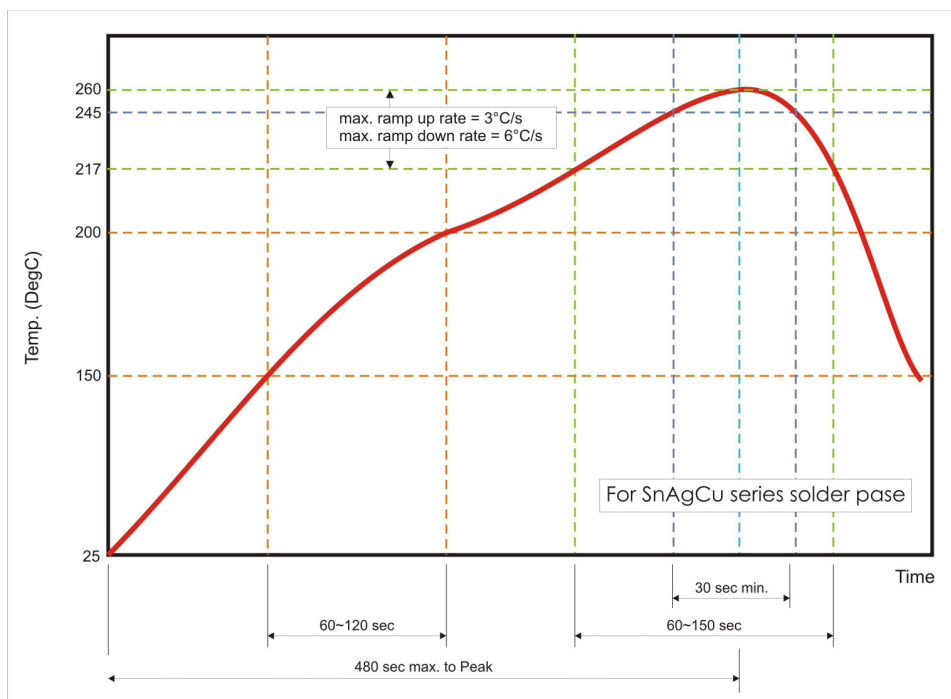


Fig 3. Recommended IR reflow soldering profile for
SMT process with SnAgCu series solder paste

TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category **LCT/UCT/56**(rated temperature range : **Lower Category Temperature**, **Upper Category Temperature**; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, sub-clause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

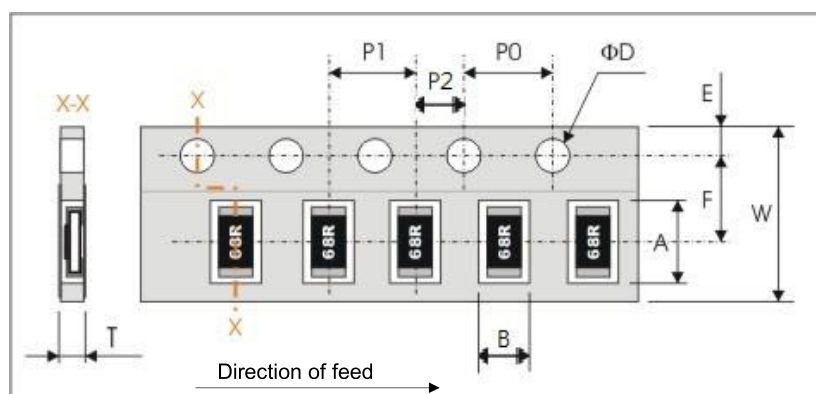
All soldering tests are performed with mildly activated flux.

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
High Temperature Exposure (Storage) MIL-STD-202 method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Temperature cycling JESD22 method JA-104	1000 cycles, -55°C ~ +155°C, dwell time 30min maximum.	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
External Visual MIL-STD-883 method 2009	Electrical test not required. Inspect device construction, marking and workmanship	No visual damage and refer WTC marking code.	
Physical Dimension JESD22 method JB-100	Verify physical dimensions(L, W, T, Tb, Tt)	Within the specified tolerance for WTC.	
Short Time Overload (STOL) JISC5201-1: 1998 Clause 4.13	Permanent resistance change after a 5 second application of a voltage 2.5 times the rated voltage or max. overload voltage, whichever is less	ΔR/R max. ±(2.0%+0.1Ω) No visible damage	<50mΩ
Resistance to soldering heat (R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C ±5°C	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
Thermal shock MIL-STD-202 method 107	Test -55 to 155°C / dwell time 15min/ Max transfer time 20sec 300cycles	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
ESD AEC-Q200-002	Test contact 1KV	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Solderability J-STD-002	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C / 5sec. b) Steam the sample dwell time 8 hour/ solder dipping 215°C / 5sec c) Steam the sample dwell time 8 hour/ solder dipping 260°C / 7sec	95% coverage min., good tinning No visible damage	
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)}$ $t_1 : 20^\circ\text{C} + 5^\circ\text{C} - 1^\circ\text{C}$ R ₁ : Resistance at reference temperature R ₂ : Resistance at test temperature	Within the specified tolerance Refer to "QUICK REFERENCE DATA"	
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending once 2mm for 60sec	ΔR/R max. ±(1.0%+0.05Ω). No visible damage	<50mΩ
Terminal strength AEC-Q200-006	Pressurizing force: 1.8Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations	
Resistance to Solvents MIL-STD-202 Method 215	Solvent is Isopropyl alcohol, immersion 3mins at 25°C and brush 10 strokes with a toothbrush with a handle made of a non-reactive material (wet bristle),immersion and brush 3 times and then air blow dry	No superficial defect on marking, encapsulation, coating, appearance. Electrical characteristics within products specification and tolerance. Inspect at 3X max. for marking, inspect at 10X for part damage	
Mechanical Shock MIL-STD-202 method 213	Test ½ Sine Pulse/ 1500g peak/ normal duration: 6ms/ Velocity: 15.4ft/sec. Three shocks in each direction, total 18 shocks	Within the specified tolerance No visible damage	<50mΩ
Vibration MIL-STD-202 method 204	Test 5g's for 20min., 12 cycles each of 3 orientations	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Sulfuration test	Oil 105°C soaked with 3.5% Sulfur substance, 500hrs	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ

PACKAGING

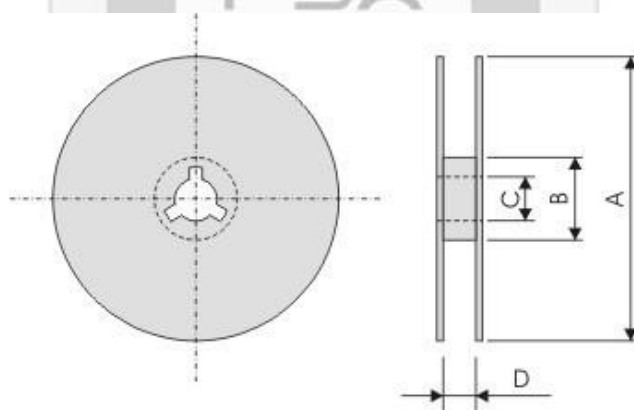
Tape specifications (unit :mm)



Series No.	A	B	W	F	E
SR25	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.10	1.75±0.10
SR20	5.50±0.20	2.80±0.20			

Series No.	P1	P0	P2	ΦD	T
SR25	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	MAX1.2
SR20					

Reel dimensions (unit :mm)



Symbol	A	B	C	D
7" Reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	12.4±1.0
10" Reel	Φ254.0±2.0	Φ100.0±1.0	13.0±0.2	14.0±0.2
13" Reel	Φ330.0±2.0	Φ100.0±1.0	13.0±0.2	14.0±0.2

Taping quantity

2512, 2010 by plastic tape taping 4,000 pcs per 7" reel; 8,000pcs per 10" reel; 16,000pcs per 13" reel.

APPROVAL SHEET

SR20X, SR25X

$\pm 1\%$, $\pm 5\%$

Power chip resistors

Size 2010, 2512

Automotive & Anti-Sulfuration

FEATURE

1. High reliability and stability $\pm 1\%$
2. Sulfuration resistant ASTM B-809-95 compliant
3. Automotive grade AEC Q-200 Compliant
4. Higher component and equipment reliability
5. RoHS compliant Lead free products

APPLICATION

- Power supply
- Industry
- Motor control
- M/B Computer
- Automotives
- Server

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. **The extra protective metal film is added onto top side electrodes to protect termination from sulfuration.** The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

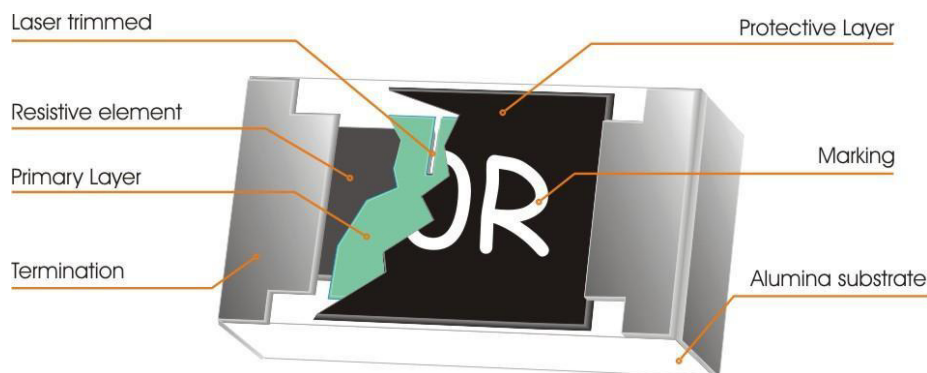


Fig 1. Construction of 2512, 2010 Chip-R

QUICK REFERENCE DATA

Item	General Specification	
Series No.	SR20	SR25
Size code	2010 (5025),	2512(6432)
Resistance Tolerance	$\pm 5\%$ (E24); $\pm 1\%$ (E24+E96)	
Resistance Range	$1\Omega \sim 10M\Omega$, Jumper (0Ω)	
TCR (ppm/°C)	$< 10\Omega$	
	± 200 ppm/°C	
	$10\Omega \sim 1M\Omega$	
	± 100 ppm/°C	
	$> 1M\Omega$	
		± 200 ppm/°C
Max. dissipation at $T_{amb}=70^\circ\text{C}$	0.75 W	1W
Max. Operation Voltage (DC or RMS)	200V	250V
Max. Overload Voltage (DC or RMS)	400V	500V
Operation temperature	$-55 \sim +155^\circ\text{C}$	

Test conditions for jumper (0 ohm)

Type	SR20X	SR25X
Power Rating At 70C	0.75 W	1 W
Resistance	Max. 50mR	Max. 50mR
Rated Current	3.2 A	4.5 A
Peak Current	8 A	11 A
Operating Temperature	$-55^\circ\text{C} \sim 155^\circ\text{C}$	$-55^\circ\text{C} \sim 155^\circ\text{C}$

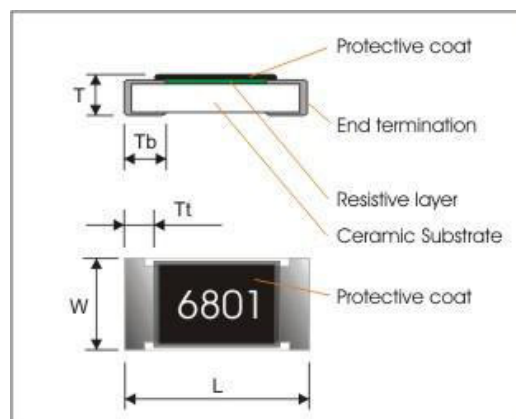
Note :

- This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$\text{RCWV} = \sqrt{\text{RatedPower} \times \text{Resistance Value}} \text{ or Max. RCWV listed above, whichever is lower.}$$

MECHANICAL DATA (unit : mm)

TYPE	SR20	SR25
L	5.00 ± 0.20	6.40 ± 0.20
W	2.50 ± 0.20	3.20 ± 0.20
T	0.55 ± 0.10	0.60 ± 0.10
Tt	0.65 ± 0.25	0.65 ± 0.25
Tb	0.60 ± 0.25	0.90 ± 0.25



MARKING

Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value $\pm 5\%$, $\pm 1\%$ tolerance !

Size	$\pm 5\%$	$\pm 1\%$
2512, 2010	4-digits marking	4-digits marking

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96 & E24 series for resistors with a tolerance of $\pm 5\%$ & $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063".

Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

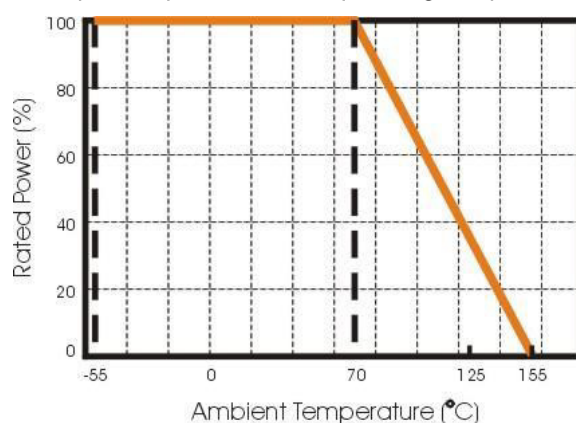


Fig.2 Maximum dissipation in percentage of rated power
As a function of the ambient temperature

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

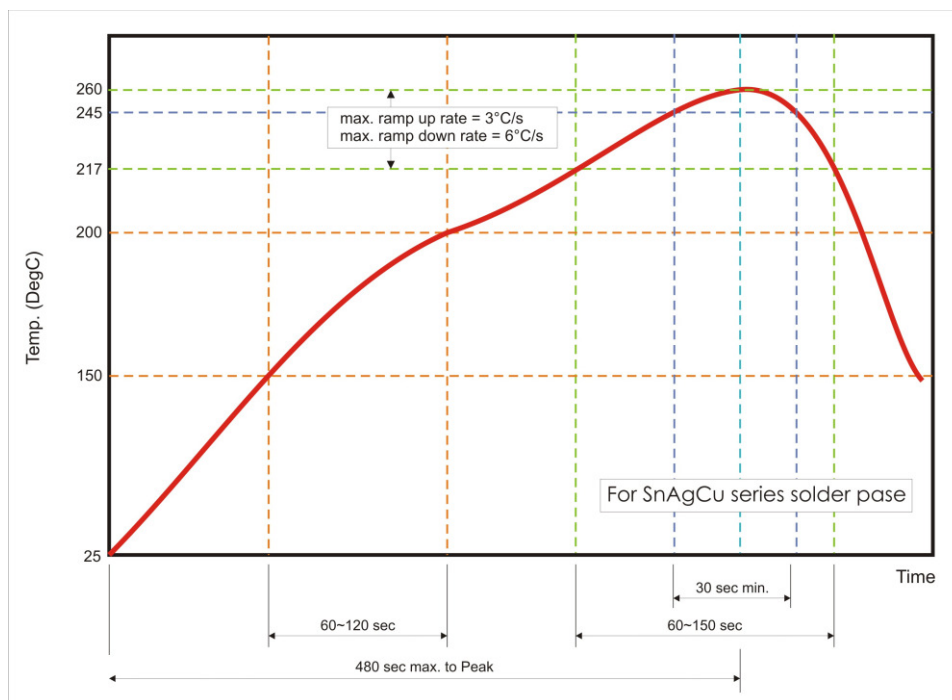


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

SR25	X	472_	J	T	L
Size code SR25 : 2512 SR20 : 2010	Type code X : ±5%: 1Ω - 10MΩ ±1%: 10Ω - 1MΩ W : ±1%: <10Ω; > 1MΩ	Resistance code E24: 2 significant digits followed by no. of zeros 100Ω = 101_ 10KΩ = 103 E96 : 3 significant digits followed by no. of zeros 102Ω = 1020 37.4KΩ = 3742	Tolerance F : ±1% J : ±5% P : Jumper	Packaging code T : 7" Reeled taping Q : 10" Reeled taping G : 13" Reeled taping B : Bulk	Termination code L = Sn base (lead free)

* Anti-Sulfuration test conditions: ASTM B-809-95 105°C, 750hrs! Acceptance criteria +/-2%!

* 100% CCD visual inspection to guarantee visual quality.

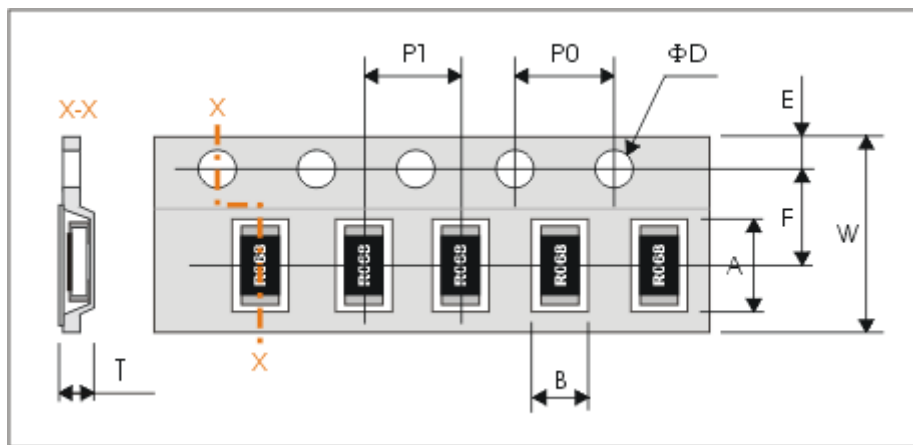
TEST AND REQUIREMENTS (AEC Q-200)

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/}^\circ\text{C)}$ $t_1 : 20^\circ\text{C}+5^\circ\text{C}-1^\circ\text{C}$ R_1 : Resistance at reference temperature R_2 : Resistance at test temperature	Within the specified tolerance Refer to "QUICK REFERENCE DATA"	N/A
Resistance to soldering heat(R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C±5°C	ΔR/R max. ±(0.5%+0.05Ω) no visible damage	<50mΩ
Solderability J-STD-002	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C/ 5sec. b) Steam the sample dwell time 8 hour/ solder dipping 215°C/ 5sec. c) Steam the sample dwell time 8 hour/ solder dipping 260°C/ 7sec.	95% coverage min., good tinning and no visible damage	
Temperature cycling JESD22 Method JA-104	1000 cycles, -55°C ~ +155°C, dwell time 30min maximum.	ΔR/R max. ±(0.5%+0.05Ω) no visible damage	<50mΩ
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
High Temperature Exposure MIL-STD-202 Method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	ΔR/R max. ±(1%+0.05Ω) No visible damage	<50mΩ
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4),bending once 2mm for 60sec.	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Terminal strength AEC-Q200-006	Pressurizing force: 1.8Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations	
Thermal shock MIL-STD-202 method 107	Test -55 to 155°C/ dwell time 15min/ Max transfer time 20sec 300cycles	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
ESD AEC-Q200-002	Test contact 3KV.	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ

Mechanical Shock MIL-STD-202 method 213	Test ½ Sine Pulse, Peak value: 100g, normal duration: 6ms, Velocity change:12.3ft/sec. Three shocks in each direction, total 18 shocks.	Within product specification tolerance and no visible damage.	<50mΩ
Vibration MIL-STD-202 method 204	Test 5g's for 20 min., 12 cycles each of 3 orientations.	△R/R max. ±(1%+0.05Ω) and no visible damage.	<50mΩ
Resistance to Solvents : MIL-STD-202 Method 215	Solvent is Isopropyl alcohol, immersion 3mins at 25 °C and brush 10 strokes with a toothbrush with a handle made of a non-reactive material (wet bristle), immersion and brush 3 times and then air blow dry.	No superficial defect on marking, encapsulation, coating, appearance. Electrical characteristics within products specification and tolerance. Inspect at 3X max. for marking, inspect at 10X for part damage.	<50mΩ
External Visual MIL-STD-883 method 2009	Electrical test not required. Inspect device construction, marking and workmanship	No visual damage and refer WTC marking code.	
Physical Dimension JESD22 method JB-100	Verify physical dimensions(L, W, T, Tb, Tt)	Within the specified tolerance for WTC.	

PACKAGING

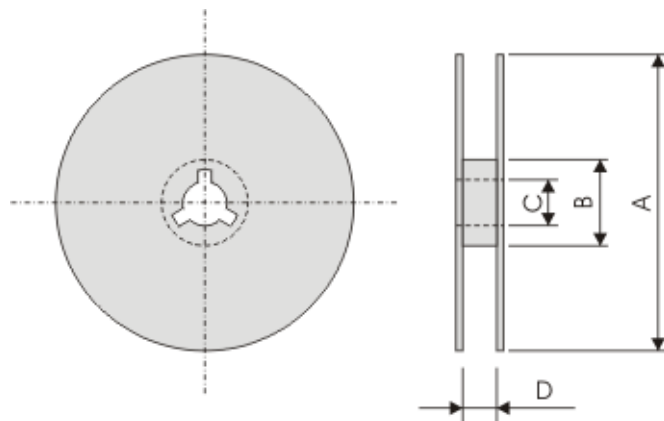
Plastic Tape specifications (unit :mm)



Type	A	B	W	F	E
SR25	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.10	1.75±0.10
SR20	5.50±0.20	2.80±0.20			

Type	P1	P0	ΦD	T
SR25	4.00±0.10	4.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	MAX1.2
SR20				

Reel dimensions



Symbol	A	B	C	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	14.0±0.2

Taping quantity

SR20, SR25 by plastic tape taping 4,000 pcs per reel.

APPROVAL SHEET

SR20X_W, SR25X_W

$\pm 1\%$, $\pm 5\%$

Power chip resistors

Size 2010, 2512

Automotive & Anti-Sulfuration

RoHS 2 Compliant with exemption 7C-1

Halogen free



*Contents in this sheet are subject to change without prior notice

FEATURE

1. High reliability and stability $\pm 1\%$
2. Sulfuration resistant ASTM B-809-95 compliant
3. Automotive grade AEC Q-200 Compliant
4. Higher component and equipment reliability
5. RoHS 2 Compliant with exemption 7C-1 and Halogen free products

APPLICATION

- Power supply
- Industry
- Motor control
- M/B Computer
- Automotives
- Server

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. **The extra protective metal film is added onto top side electrodes to protect termination from sulfuration.** The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

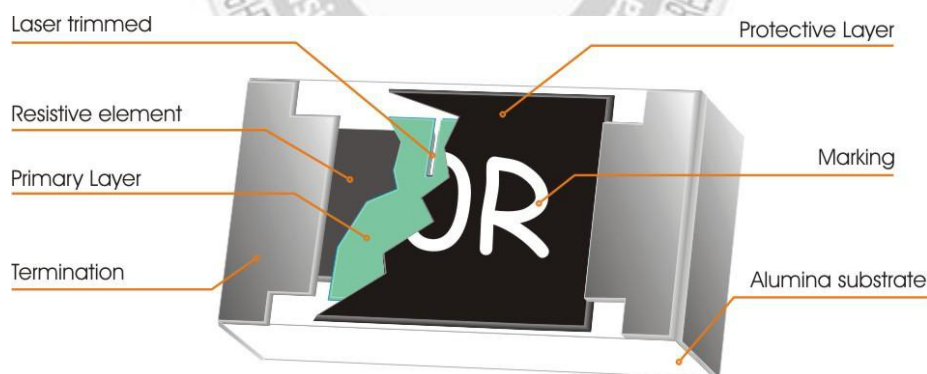


Fig 1. Construction of 2512, 2010 Chip-R

QUICK REFERENCE DATA

Item	General Specification	
Series No.	SR20	SR25
Size code	2010 (5025),	2512(6432)
Resistance Tolerance	$\pm 5\%$ (E24); $\pm 1\%$ (E24+E96)	
Resistance Range	$1\Omega \sim 10M\Omega$, Jumper (0Ω)	
TCR (ppm/°C)	$< 10\Omega$	
	$10\Omega \sim 1M\Omega$	
	$> 1M\Omega$	
	± 200 ppm/°C	
	± 100 ppm/°C	
	± 200 ppm/°C	
Max. dissipation at $T_{amb}=70^\circ\text{C}$	0.75 W	1W
Max. Operation Voltage (DC or RMS)	200V	250V
Max. Overload Voltage (DC or RMS)	400V	500V
Operation temperature	$-55 \sim +155^\circ\text{C}$	

Test conditions for jumper (0 ohm)

Type	SR20X	SR25X
Power Rating At 70C	0.75 W	1 W
Resistance	Max. 50mR	Max. 50mR
Rated Current	3.2 A	4.5 A
Peak Current	8 A	11 A
Operating Temperature	$-55^\circ\text{C} \sim 155^\circ\text{C}$	$-55^\circ\text{C} \sim 155^\circ\text{C}$

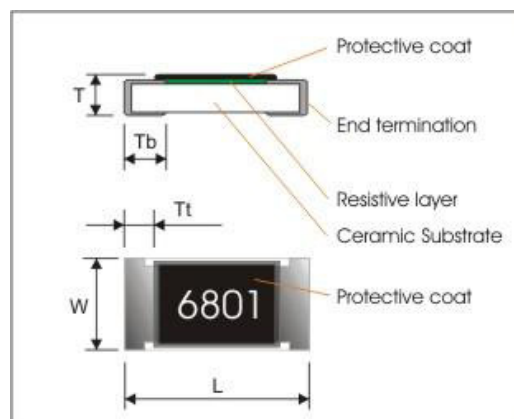
Note :

1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$\text{RCWV} = \sqrt{\text{Rated Power} \times \text{Resistance Value}} \text{ or Max. RCWV listed above, whichever is lower.}$$

MECHANICAL DATA (Unit: mm)

TYPE	SR20	SR25
L	5.00 ± 0.20	6.40 ± 0.20
W	2.50 ± 0.20	3.20 ± 0.20
T	0.55 ± 0.10	0.60 ± 0.10
Tt	0.65 ± 0.25	0.65 ± 0.25
Tb	0.60 ± 0.25	0.90 ± 0.25



MARKING

Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value $\pm 5\%$, $\pm 1\%$ tolerance !

Size	$\pm 5\%$	$\pm 1\%$
2512, 2010	4-digits marking	4-digits marking

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96 & E24 series for resistors with a tolerance of $\pm 5\%$ & $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063".

Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

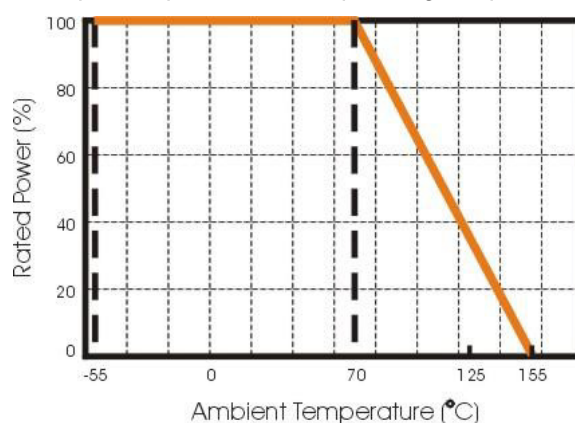


Fig.2 Maximum dissipation in percentage of rated power
As a function of the ambient temperature

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

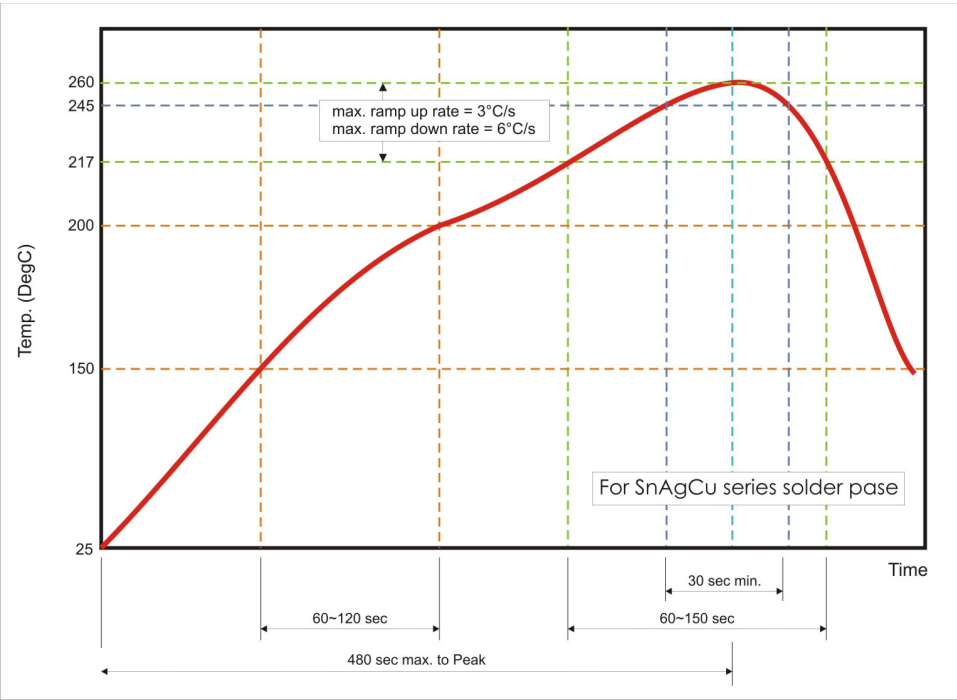


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

SR25	X	472_	J	T	L	W
Size code SR25 : 2512 SR20 : 2010	Type code X : ±5%: 1Ω - 10MΩ ±1%: 10Ω - 1MΩ W : ±1%: <10Ω; > 1MΩ	Resistance code E24: 2 significant digits followed by no. of zeros 100Ω = 101_ 10KΩ = 103 E96 : 3 significant digits followed by no. of zeros 102Ω =1020 37.4KΩ =3742	Tolerance F : ±1% J : ±5% P : Jumper	Packaging code T : 7" Reeled taping Q : 10" Reeled taping G : 13" Reeled taping B : Bulk	Termination code L = Sn base (lead free)	Special code W = special termination a, b control

Product Dimension a, b

端电极上爬长度	测量右图的a值		0603及以上规格: 大于35μm 0402: 大于25μm; 0201:大于15μm
最外层保护膜在交界面处的厚度	测量右图的b值		0603及以上规格: 大于7μm 0402、0201: 大于6μm

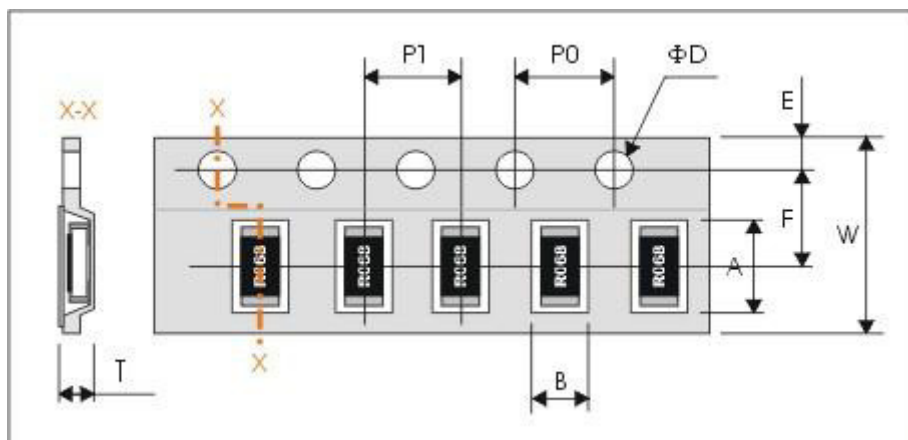
TEST AND REQUIREMENTS (AEC Q-200)

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/}^\circ\text{C)}$ $t_1 : 20^\circ\text{C}+5^\circ\text{C}-1^\circ\text{C}$ $R_1 : \text{Resistance at reference temperature}$ $R_2 : \text{Resistance at test temperature}$	Within the specified tolerance Refer to "QUICK REFERENCE DATA"	N/A
Resistance to soldering heat(R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C±5°C	ΔR/R max. ±(0.5%+0.05Ω) no visible damage	<50mΩ
Solderability J-STD-002	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C/ 5sec. b) Steam the sample dwell time 8 hour/ solder dipping 215°C/ 5sec. c) Steam the sample dwell time 8 hour/ solder dipping 260°C/ 7sec.	95% coverage min., good tinning and no visible damage	
Temperature cycling JESD22 Method JA-104	1000 cycles, -55°C ~ +155°C, dwell time 30min maximum.	ΔR/R max. ±(0.5%+0.05Ω) no visible damage	<50mΩ
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
High Temperature Exposure MIL-STD-202 Method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	ΔR/R max. ±(1%+0.05Ω) No visible damage	<50mΩ
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4),bending once 2mm for 60sec.	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Terminal strength AEC-Q200-006	Pressurizing force: 1.8Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations	
Thermal shock MIL-STD-202 method 107	Test -55 to 155°C/ dwell time 15min/ Max transfer time 20sec 300cycles	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
ESD AEC-Q200-002	Test contact 3KV.	$\Delta R/R$ max. $\pm(1.0\%+0.05\Omega)$ No visible damage	<50mΩ
Mechanical Shock MIL-STD-202 method 213	Test ½ Sine Pulse, Peak value: 100g, normal duration: 6ms, Velocity change:12.3ft/sec. Three shocks in each direction, total 18 shocks.	Within product specification tolerance and no visible damage.	<50mΩ
Vibration MIL-STD-202 method 204	Test 5g's for 20 min., 12 cycles each of 3 orientations.	$\Delta R/R$ max. $\pm(1\%+0.05\Omega)$ and no visible damage.	<50mΩ
Resistance to Solvents : MIL-STD-202 Method 215	Solvent is Isopropyl alcohol, immersion 3mins at 25 °C and brush 10 strokes with a toothbrush with a handle made of a non-reactive material (wet bristle), immersion and brush 3 times and then air blow dry.	No superficial defect on marking, encapsulation, coating, appearance. Electrical characteristics within products specification and tolerance. Inspect at 3X max. for marking, inspect at 10X for part damage.	<50mΩ
External Visual MIL-STD-883 method 2009	Electrical test not required. Inspect device construction, marking and workmanship	No visual damage and refer WTC marking code.	
Physical Dimension JESD22 method JB-100	Verify physical dimensions(L, W, T, Tb, Tt)	Within the specified tolerance for WTC.	
Short Time Overload (STOL) JISC5201-1: 1998 Clause 4.13	2.5 times RCWV or max. overload voltage, for 5seconds	$\pm 5\%$: $\Delta R/R$ max. $\pm(2\%+0.05\Omega)$ $\pm 1\%$: $\Delta R/R$ max. $\pm(1\%+0.05\Omega)$ No visible damage	<50mΩ
ESD AEC-Q200-002	Test contact 1.0KV (0.5KV for 0402 only)	$\Delta R/R$ max. $\pm(1.0\%+0.05\Omega)$ No visible damage	<50mΩ
Sulfuration test ASTM B-809	105°C 1000hrs against EIA-977-2016 condition B	$\Delta R/R$ max. $\pm(2.0\%+0.05\Omega)$ No visible damage	<50mΩ

PACKAGING

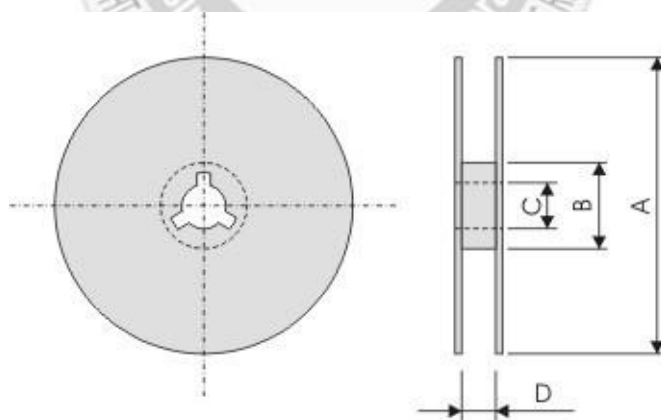
Plastic Tape specifications (unit :mm)



Type	A	B	W	F	E
SR25	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.10	1.75±0.10
SR20	5.50±0.20	2.80±0.20			

Type	P1	P0	ΦD	T
SR25	4.00±0.10	4.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	MAX1.2
SR20				

Reel dimensions



Symbol	A	B	C	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	14.0±0.2

Taping quantity

SR20, SR25 by plastic tape taping 4,000 pcs per reel.

APPROVAL SHEET

SR12, SR10, SR08, SR06, SR04

$\pm 1\%$, $\pm 5\%$

Thick Film Anti-Sulfur Chip Resistors

Size 1206, 1210, 0805, 0603, 0402

(Automotive & Anti-Sulfuration)

RoHS 2 Compliant with exemption 7C-I
Halogen free

FEATURE

1. High reliability and stability $\pm 1\%$
2. Sulfuration resistant by Oil 105°C soaked with 3.5% sulphur substance 500hrs
3. Sulfuration resistant by ASTM B-809 105°C 1000hrs
4. Automotive grade AEC Q-200 Qualified
5. 100% CCD inspection
6. RoHS 2 Compliant with exemption 7C-I and Halogen free products

APPLICATION

- Automotive application
- Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The extra protective metal film is added onto top side electrodes to protect termination from sulfuration. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

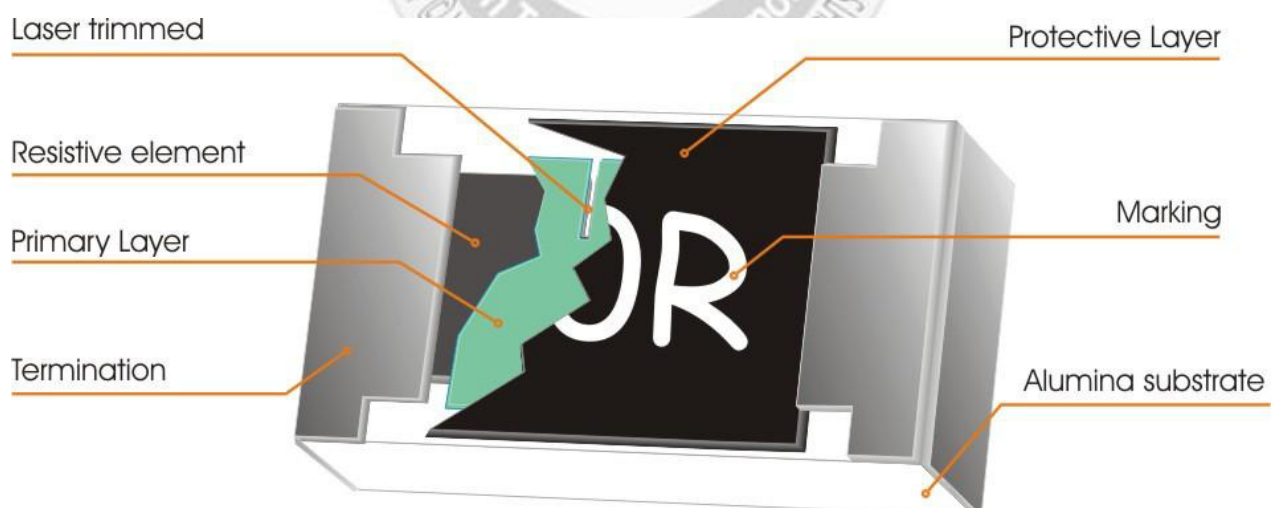


Fig 1. Construction of a Chip-R

QUICK REFERENCE DATA

Item	General Specification				
Series No.	SR10_A	SR12_A	SR08_A	SR06_A	SR04_A
Size code	1210(3225)	1206(3216)	0805(2012)	0603(1608)	0402(1005)
Resistance Range	1Ω~10MΩ				
Resistance Tolerance	± 1%, E96+E24 ± 5%, E24				
TCR (ppm/°C) 10Ω < R ≤ 10MΩ R ≤ 10Ω	≤ ± 100 -200~+400	≤ ± 100 -200~+400	≤ ± 100 -200~+400	≤ ± 100 -200~+400	≤ ± 100 -200~+400
Max. dissipation @ T _{amb} =70°C	1/2 W	1/4 W	1/4 W	1/8 W	1/10 W
Max. Operation Voltage (DC or RMS)	200V	200V	150V	75V	50V
Max. Overload Voltage (DC or RMS)	400V	400V	300V	150V	100V
Climatic category (IEC 60068)	55/155/56				

Note :

- This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

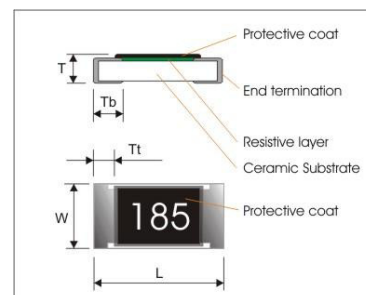
$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}}$$
or Max. RCWV listed above, whichever is lower.

Jumpers (0Ω)

Item	General Specification				
Series No.	SR10_A	SR12_A	SR08_A	SR06_A	SR04_A
Size code	1210(3225)	1206(3216)	0805(2012)	0603(1608)	0402(1005)
Resistance Range	MAX.50mΩ	MAX.50mΩ	MAX.50mΩ	MAX.50mΩ	MAX.50mΩ
TCR (ppm/°C)	TCR is not applicable for Jumper product				
Max. dissipation @ T _{amb} =70°C	1/2 W	1/4 W	1/4 W	1/8 W	1/10 W
Max Rated Current (Rated Current)	2.5A	2A	1.5A	1A	1A
Max Overload Current (Peak Current)	6A	5A	3.5A	3A	2A
Operation temperature	- 55~+155°C				

DIMENSIONS (unit : mm)

	SR10	SR12	SR08	SR06	SR04
L	3.10 ± 0.10	3.10 ± 0.10	2.00 ± 0.10	1.60 ± 0.10	1.00 ± 0.05
W	2.60 ± 0.10	1.60 ± 0.10	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
T	0.55 ± 0.10	0.60 ± 0.15	0.50 ± 0.15	0.45 ± 0.15	0.35 ± 0.05
Tb	0.50 ± 0.20	0.45 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.25 ± 0.10
Tt	0.50 ± 0.20	0.50 ± 0.20	0.40 ± 0.20	0.30 ± 0.10	0.20 ± 0.10



CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

SR12	X	1020	F	T	L	A
Automotive code	Type code	Resistance code	Tolerance	Packaging code	Termination code	Special code
SR10 : 1210	X :	±5%, E24: 2 significant digits followed by no. of zeros and a blank	F : ±1%	T : 7" Reeled taping	L = Sn base (lead free)	A = Anti-sulfur by Oil 105°C soaked with 3.5% sulphur 500hrs
SR12 : 1206	±1%, 10-1M	220Ω = 221_	J : ±5%	Q : 10" Reeled taping		EIA-977-2016 condition B
SR08 : 0805	±5%, 1-10M	("_" means a blank)	P : Jumper	G : 13" Reeled taping		
SR06 : 0603	W :	±1%, E24+E96: 3 significant digits followed by no. of zeros		H : 13" reel 50Kpcs only for 0402		
SR04: 0402	±1%, <10, >1M	E24: 100Ω= 1000 E96: 37.4KΩ= 3742		B : Bulk		
				D : 7" reel 20Kpcs only for 0402		
				A : 7" reel 15Kpcs only for 0402		

Remark:

1210, 1206, 0805, 0603:

1. 8mm width paper taping 5000pcs per 7" reel, 10kpcs per 10" reel, 20kpcs per 13" reel.
2. Bulk packaging: 5000pcs per poly-bag

0402:

1. 8mm width paper taping 10,000pcs per 7" reel, 70kpcs per 13" reel.
2. Bulk packaging: 5000pcs per poly-bag

MARKING

Size \ Nr. Of digit of code\tolerance	±5% & Jumper (0Ω)	±1%
1210 (3225)	3 digits marking	4 digits marking
1206 (3216)	3 digits marking	4 digits marking
0805 (2012)	3 digits marking	4 digits marking
0603 (1608)	3 digits marking	3 digits marking
0402 (1005)	NO MARKING	

3 digits marking 【±5% : 1210,1206, 0805, 0603 & Jumper (0Ω)】

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value.

3 digits marking (±1% : 0603)

Nominal resistance	Description																																						
1.E24 series	2 significant digits followed by No. of zeros .As 0603 WR06X ±5% Example <table><tr><td>RESISTANCE</td><td>4.7Ω</td><td>47Ω</td><td>470Ω</td><td>4K70</td><td>47K0</td><td>470K</td><td>4M70</td></tr><tr><td>3 digits marking</td><td>4R7</td><td>470</td><td>471</td><td>472</td><td>473</td><td>474</td><td>475</td></tr></table>	RESISTANCE	4.7Ω	47Ω	470Ω	4K70	47K0	470K	4M70	3 digits marking	4R7	470	471	472	473	474	475																						
RESISTANCE	4.7Ω	47Ω	470Ω	4K70	47K0	470K	4M70																																
3 digits marking	4R7	470	471	472	473	474	475																																
2.E96 series	The 1st two digit codes are referring to the CODE on the table, the 3rd code is the index of resistance value. Repeat values between E24 and E96 series, whose marking are based on the E96 CODE table. <table><tr><td>Code</td><td>Z</td><td>Y</td><td>X</td><td>A</td><td>B</td><td>C</td><td>D</td><td>E</td><td>F</td><td>G</td></tr><tr><td>Multiplier</td><td>10⁻³</td><td>10⁻²</td><td>10⁻¹</td><td>10⁰</td><td>10¹</td><td>10²</td><td>10³</td><td>10⁴</td><td>10⁵</td><td>10⁶</td></tr></table> Example <table><tr><td>RESISTANCE</td><td>1.78Ω</td><td>17.8Ω</td><td>178Ω</td><td>1K78</td><td>17K8</td><td>178K</td><td>1M78</td></tr><tr><td>3 digits marking</td><td>25Y</td><td>25X</td><td>25A</td><td>25B</td><td>25C</td><td>25D</td><td>25E</td></tr></table>	Code	Z	Y	X	A	B	C	D	E	F	G	Multiplier	10 ⁻³	10 ⁻²	10 ⁻¹	10 ⁰	10 ¹	10 ²	10 ³	10 ⁴	10 ⁵	10 ⁶	RESISTANCE	1.78Ω	17.8Ω	178Ω	1K78	17K8	178K	1M78	3 digits marking	25Y	25X	25A	25B	25C	25D	25E
Code	Z	Y	X	A	B	C	D	E	F	G																													
Multiplier	10 ⁻³	10 ⁻²	10 ⁻¹	10 ⁰	10 ¹	10 ²	10 ³	10 ⁴	10 ⁵	10 ⁶																													
RESISTANCE	1.78Ω	17.8Ω	178Ω	1K78	17K8	178K	1M78																																
3 digits marking	25Y	25X	25A	25B	25C	25D	25E																																
3. Remark	There is no marking for the items not under E24 and E96 series.																																						

E96 CODE table:															
CODE	R value	CODE	R-value	CODE	R-Value	CODE	R-value	CODE	R-value	CODE	R-value	CODE	R-value	CODE	R-value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

4 digits marking (±1% : 1210,1206,0805)

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value. For values below 97Ω the R is used as a digit. For values of 100Ω or greater, the first 3 digits are significant, and the fourth digit indicates the number of multiple to follow.

Example

RESISTANCE	Jumper (0Ω)	4.7Ω	10Ω	12Ω	100Ω	6800Ω	47000Ω	470000Ω
3 digits marking (1210, 1206, 0805, 0603 ±5% & Jumper)	000	4R7	100	120	101	682	473	474
4 digits marking	-	4R70	10R0	12R0	1000	6801	4702	4703

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E24& E96 series for resistors with a tolerance of $\pm 5\%$, $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063"

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

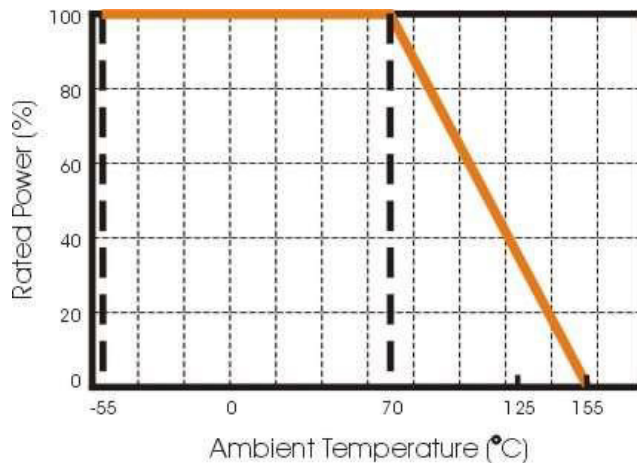


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

Storage and Handling Conditions:

- Products are recommended to be used up within two years since operation date as ensured shelf life. Check solderability in case shelf life extension is needed.
- To store products with following condition:
 - Temperature :5 to 40°C
 - Humidity :20 to 70% relative humidity
- Caution:
 - Don't store products in a corrosive environment such as sulfide, chloride gas, or acid.
It may cause oxidation of electrode, which easily be resulted in poor soldering.
 - To store products on the shelf and avoid exposure to moisture.
 - Don't expose products to excessive shock, vibration, direct sunlight and so on.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

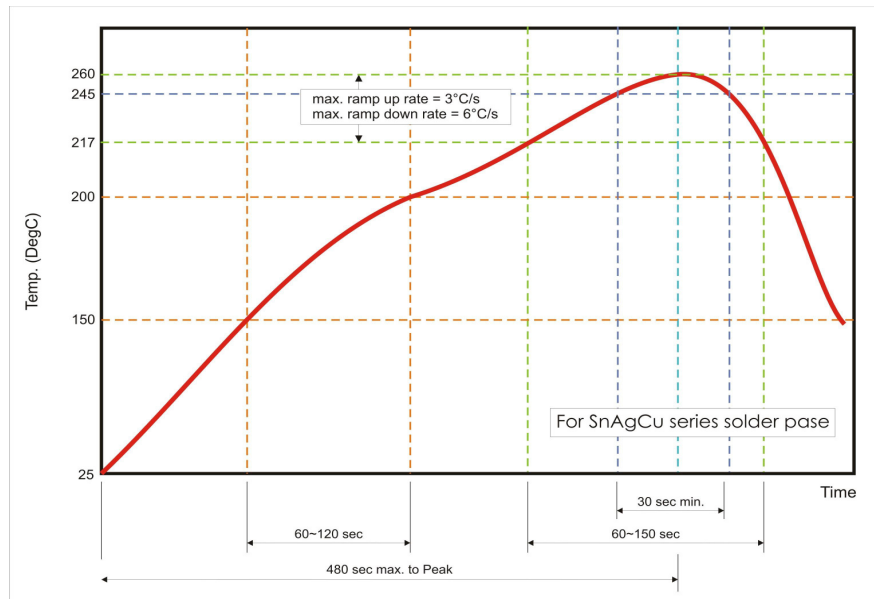


Fig 3. Infrared soldering profile for Chip Resistors
SMT process with SnAgCu series solder paste

TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category **LCT/UCT/56**(rated temperature range : **Lower Category Temperature**, **Upper Category Temperature**; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, sub-clause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

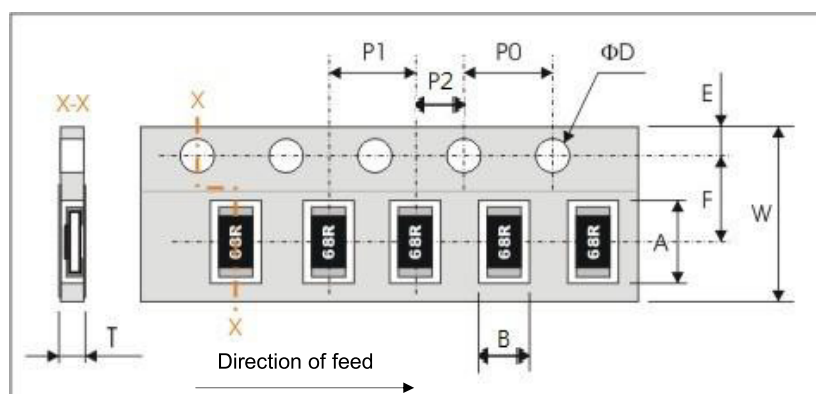
All soldering tests are performed with mildly activated flux.

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)}$ $t_1 : 20^\circ\text{C} \pm 5^\circ\text{C} - 1^\circ\text{C}$ R_1 : Resistance at reference temperature R_2 : Resistance at test temperature	Within the specified tolerance Refer to "QUICK REFERENCE DATA"	
Resistance to soldering heat (R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C ±5°C	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Solderability J-STD-002	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C / 5sec. b) Steam the sample dwell time 1 hour/ solder dipping 215°C / 5sec. c) Steam the sample dwell time 1 hour/ solder dipping 260°C / 7sec.	95% coverage min., good tinning No visible damage	
Temperature cycling JESD22 method JA-104	1000 cycles, -55°C ~ +155°C, dwell time 5~10min	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
High Temperature Exposure MIL-STD-202 method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Mechanical Shock MIL-STD-202 method 213	1/2 Sine Pulse / 1500g Peak / Velocity 15.4ft/sec	Within the specified tolerance No visible damage	<50mΩ
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending once 2mm for 10sec	ΔR/R max. ±(1.0%+0.05Ω). No visible damage	<50mΩ
Terminal strength AEC-Q200-006	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations	
Vibration MIL-STD-202 method 204	Test 5g's for 20min., 12 cycles each of 3 orientations	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Thermal shock MIL-STD-202 method 107	Test -55 to 155°C / dwell time 15min/ Max transfer time 20sec 300cycles	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
ESD AEC-Q200-002	Test contact 1.0KV (0.5KV for 0402 only)	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Sulfuration test	1. Oil 105°C soaked with 3.5% sulphur , 500hrs 2. ASTM B-809 105°C, 1000hrs against EIA-977-2016 condition B	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ

PACKAGING

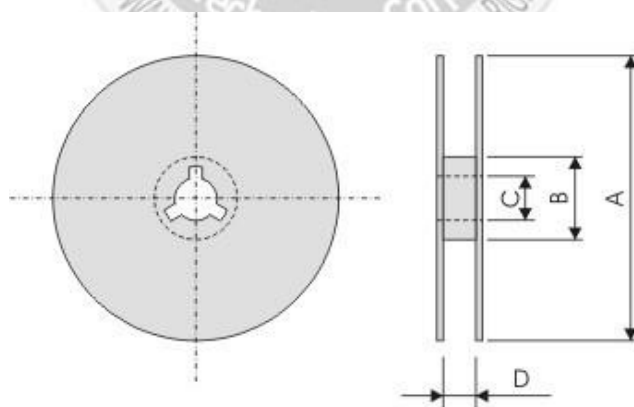
Paper Tape specifications (unit :mm)



Series No.	A	B	W	F	E
SR10	3.60±0.20	3.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10
SR12	3.60±0.20	2.00±0.20			
SR08	2.40±0.20	1.65±0.20			
SR06	1.90±0.20	1.10±0.20			
SR04	1.20±0.10	0.70±0.10			

Series No.	P1	P0	P2	ΦD	T
SR10/12/08	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	0.80±0.1
SR06					0.70±0.05
SR04	2.00±0.10				0.50±0.05

Reel dimensions (unit :mm)



Symbol	A	B	C	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5
10" reel	Φ254.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5
13" reel	Φ330.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5

APPROVAL SHEET

SR12, SR10, SR08, SR06, SR04

$\pm 1\%$, $\pm 5\%$

Thick Film High Grade General Purpose
Chip Resistors

Size 1206, 1210, 0805, 0603, 0402

Automotive & Military Compliant

Anti-Sulfuration

RoHS 2 Compliant with exemption 7C-I

Halogen free

*Contents in this sheet are subject to change without prior notice.

FEATURE

1. High reliability and stability $\pm 1\%$
2. Sulfuration resistant ASTM B-809 105°C 750hrs
3. Automotive AEC Q-200 & Military MIL-STD Compliant
4. 100% CCD inspection
5. RoHS 2 compliant with exemption 7C-I and Halogen free products

APPLICATION

- Automotive application
- Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The extra protective metal film is added onto top side electrodes to protect termination from sulfuration. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

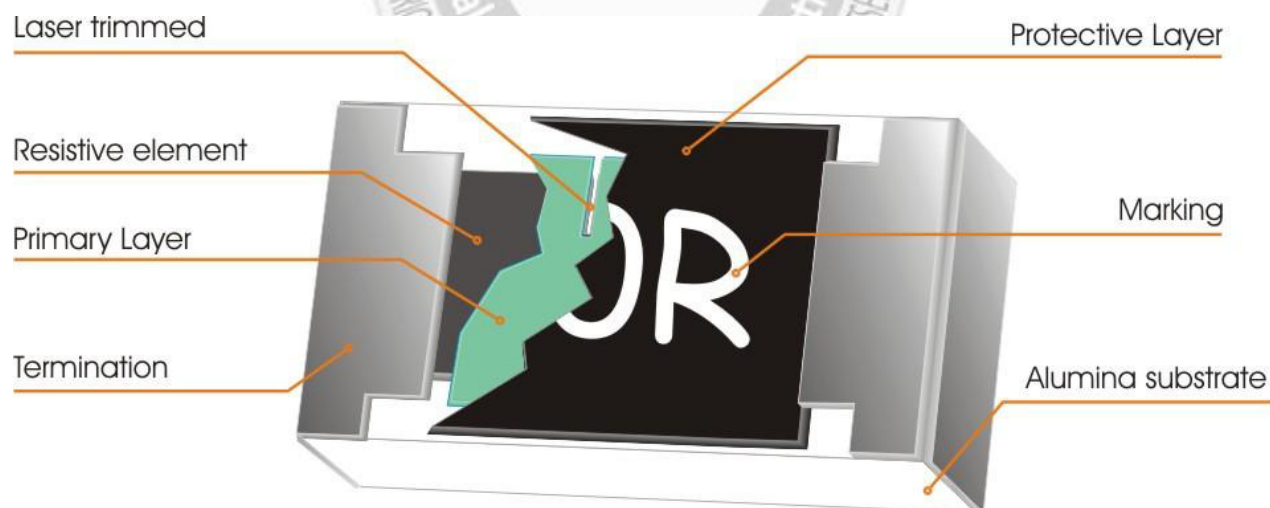


Fig 1. Construction of a Chip-R

QUICK REFERENCE DATA

Item	General Specification									
Series No.	SR10		SR12		SR08		SR06		SR04	
Size code	1210(3225)		1206(3216)		0805(2012)		0603(1608)		0402(1005)	
Resistance Range	1Ω~10MΩ (±5% tolerance), Jumper 1Ω~10MΩ (±1% tolerance)									
Resistance Tolerance	±1% E96/E24	±5% E24	±1% E96/E24	±5% E24	±1% E96/E24	±5% E24	±1% E96/E24	±5% E24	±1% E96/E24	±5% E24
TCR (ppm/°C)										
R > 1MΩ	≤ ± 200		≤ ± 200		≤ ± 200		≤ ± 200		≤ ± 200	
10Ω < R ≤ 1MΩ	≤ ± 100		≤ ± 100		≤ ± 100		≤ ± 100		≤ ± 100	
R ≤ 10Ω	-200~+400		-200~+400		-200~+400		-200~+400		-200~+400	
Max. dissipation @ T _{amb} =70°C	1/2 W		1/4 W		1/4 W		1/10 W		1/10 W	
Max. Operation Voltage (DC or RMS)	200V		200V		150V		75V		50V	
Max. Overload Voltage (DC or RMS)	400V		400V		300V		150V		100V	
Climatic category (IEC 60068)	55/155/56									

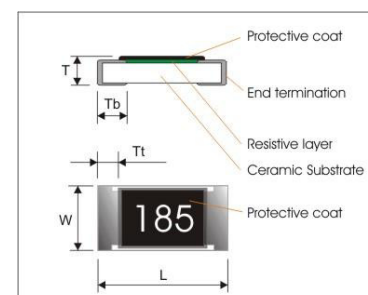
Note :

- This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}}$$
or Max. RCWV listed above, whichever is lower.
- The resistance of Jumper is defined <0.05Ω.

DIMENSIONS (unit : mm)

	SR10	SR12	SR08	SR06	SR04
L	3.10 ± 0.10	3.10 ± 0.10	2.00 ± 0.10	1.60 ± 0.10	1.00 ± 0.05
W	2.60 ± 0.10	1.60 ± 0.10	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
T	0.55 ± 0.10	0.60 ± 0.15	0.50 ± 0.15	0.45 ± 0.15	0.35 ± 0.05
Tb	0.50 ± 0.20	0.45 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.25 ± 0.10
Tt	0.50 ± 0.20	0.50 ± 0.20	0.40 ± 0.20	0.30 ± 0.10	0.20 ± 0.10



MARKING

Size \ Nr. Of digit of code\tolerance	±5%	±1%
1210/1206/0805	3-digits marking	4-digits marking
0603 (1608)	3-digits marking	3-digits marking
0402(1005)	NO MARKING	

3-digits marking (±5% : 1210 & 1206 & 0805 & 0603)

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value.

3-digits marking (±1% : 0603)

Nominal resistance				Description											
1.E-24 series				As 0603 WR06X ±5%.											
2.E-96 series				The 1 st two digit codes are referring to the CODE on the table, the 3 rd code is the index of resistance value :											
				Y=10 ⁻² , X=10 ⁻¹ , A=10 ⁰ , B=10 ¹ , C=10 ² , D=10 ³ , E=10 ⁴ , F=10 ⁵											
				EX : 17.8Ω=25X, 178Ω=25A, 1K78 =25B 17K8=25C , 178K=25D, 1M78=25E											
3. Remark				There is no marking for the items are not under E-24 and E-96 series											
CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

4-digits marking (±1% : 1210/1206/0805)

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value.

Example

RESISTANCE	10Ω	12Ω	100Ω	6800Ω	47000Ω
3-digits marking (1210/1206/0805/0603 ±5%)	100	120	101	682	473
4-digits marking	10R0	12R0	1000	6801	4702

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of $\pm 5\%$, and E96 series for resistors with a tolerance of $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063"

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

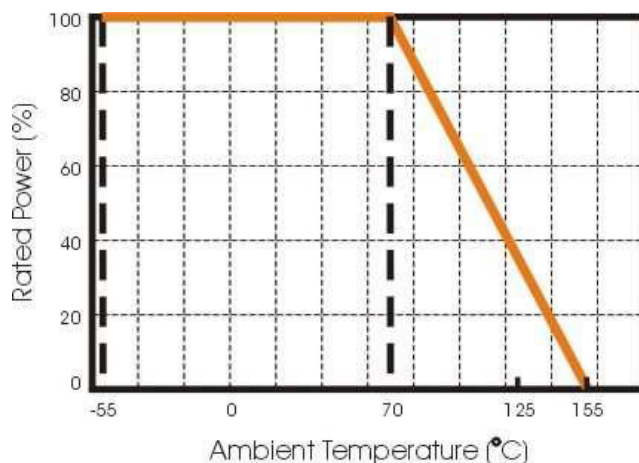


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

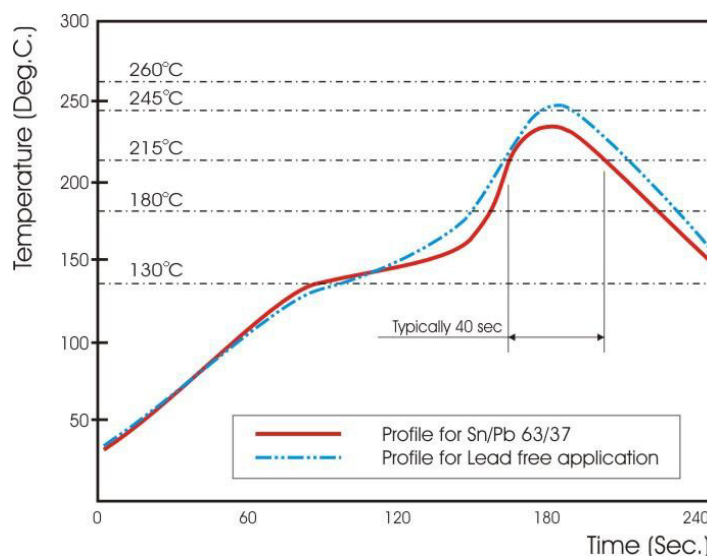


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

SR12	X	472_	J	T	L
Automotive code	Type code	Resistance code	Tolerance	Packaging code	Termination code
SR10 : 1210	X :	±5%, E24: 2 significant digits	F : ±1%	T : 7" Reeled taping	L = Sn base
SR12 : 1206	Jumper	followed by no. of	J : ±5%	Q : 10" Reeled taping	(lead free)
SR08 : 0805	±5%, 1Ω ~ 10MΩ	zeros and a blank	P : Jumper	G : 13" Reeled taping	
SR06 : 0603	±1%, 10Ω ~ 1MΩ	220Ω = 221_		B : Bulk	
SR04: 0402	W :	("_" means a blank)			
	±1%, < 10Ω; >1MΩ	±1%, E24+E96: 3 significant digits followed by no. of zeros			
		E24: 102Ω = 1020			
		E96: 37.4KΩ = 3742			

* Anti-Sulfur test conditions: ASTM B-809 105°C, RH 90%, 750hrs, acceptance criteria: +/-1.0%.

SR10, SR12, SR08, SR06:

1. Reeled tape packaging : 8mm width paper taping 5000pcs per 7" reel, 10kpcs per 10" reel, 20kpcs per 13" reel.
2. Bulk packaging : 5000pcs per poly-bag

SR04:

1. Reeled tape packaging : 8mm width paper taping 10,000pcs per reel.
2. Bulk packaging : 10,000pcs per poly-bag

TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category **LCT/UCT/56**(rated temperature range : **Lower Category Temperature, Upper Category Temperature**; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, sub-clause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

All soldering tests are performed with mildly activated flux.

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	Ω
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} \quad t_1 : 20^\circ\text{C} + 5^\circ\text{C} - 1^\circ\text{C}$ R ₁ : Resistance at reference temperature R ₂ : Resistance at test temperature	Within the specified tolerance Refer to "QUICK REFERENCE DATA"	
Resistance to soldering heat (R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C±5°C	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Solderability J-STD-002	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C / 5sec. b) Steam the sample dwell time 1 hour/ solder dipping 215°C / 5sec. c) Steam the sample dwell time 1 hour/ solder dipping 260°C / 7sec.	95% coverage min., good tinning No visible damage	
Temperature cycling JESD22 method JA-104	1000 cycles, -55°C ~ +155°C, dwell time 5~10min	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ

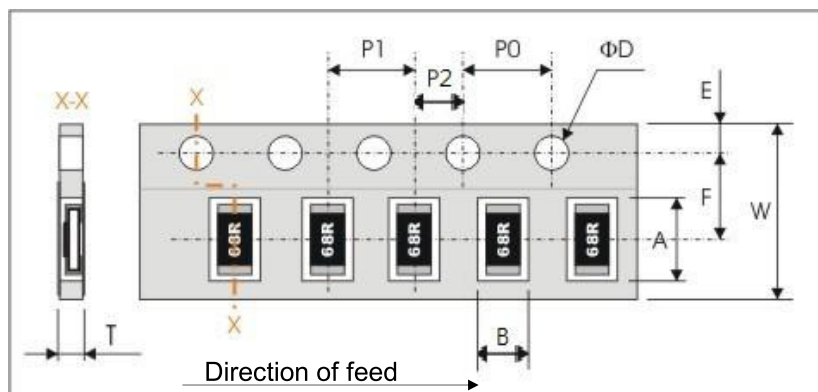
TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
High Temperature Exposure MIL-STD-202 method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Mechanical Shock MIL-STD-202 method 213	1/2 Sine Pulse / 1500g Peak / Velocity 15.4ft/sec	Within the specified tolerance No visible damage	<50mΩ
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending once 2mm for 60sec	ΔR/R max.±(1.0%+0.05Ω). No visible damage	<50mΩ
Terminal strength AEC-Q200-006	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations	
Vibration MIL-STD-202 method 204	Test 5g's for 20min., 12 cycles each of 3 orientations	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Thermal shock MIL-STD-202 method 107	Test -55 to 155°C/ dwell time 15min/ Max transfer time 20sec 300cycles	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Short Time Overload (STOL) JISC5201-1: 1998 Clause 4.13	2.5 times RCWV or max. overload voltage, for 5seconds	±5%: ΔR/R max. ±(2%+0.05Ω) ±1%: ΔR/R max. ±(1%+0.05Ω) No visible damage	<50mΩ
ESD AEC-Q200-002	Test contact 1.0KV (0.5KV for 0402 only)	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Sulfuration test	ASTM B-809 105°C, 750hrs	ΔR/R max. ±(2.0%+0.05Ω) No visible damage	<50mΩ

TEST CONDITION FOR JUMPER (0 Ω)

Item	SR10	SR12	SR08	SR06	SR04
Power Rating At 70°C	1/3W	1/4W	1/8W	1/10W	1/16W
Resistance	MAX.50mΩ				
Rated Current	2.5A	2A	1.5A	1A	1A
Peak Current	6A	5A	3.5A	3A	2A
Operating Temperature	-55 ~ +155°C				

PACKAGING

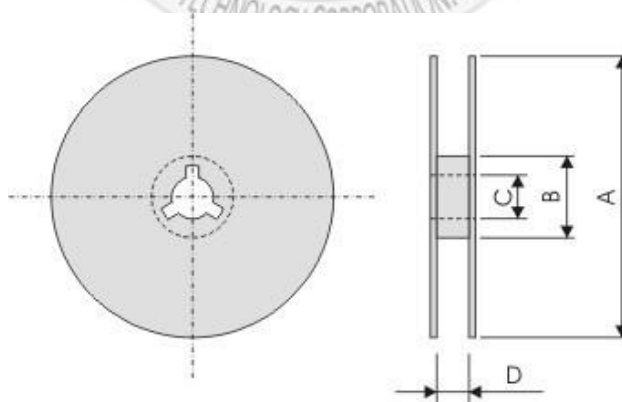
Paper Tape specifications (unit :mm)



Series No.	A	B	W	F	E
SR10	3.60±0.20	3.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10
SR12	3.60±0.20	2.00±0.20			
SR08	2.40±0.20	1.65±0.20			
SR06	1.90±0.20	1.10±0.20			
SR04	1.20±0.10	0.70±0.10			

Series No.	P1	P0	P2	ΦD	T
SR10/12/08	4.00±0.10	4.00±0.10	P2=1/2P0	Φ1.50 ^{+0.1} _{-0.0}	0.80±0.1
SR06					0.70±0.05
SR04	2.00±0.10				0.50±0.05

Reel dimensions



Symbol	A	B	C	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5
10" reel	Φ254.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5
13" reel	Φ330.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5

APPROVAL SHEET

SR12, SR10, SR08, SR06, SR04

$\pm 1\%$, $\pm 5\%$

Thick Film Anti-Sulfur Chip Resistors

Size 1206, 1210, 0805, 0603, 0402

(Automotive & Anti-Sulfuration)

RoHS 2 Compliant with exemption 7C-I

Halogen free

*Contents in this sheet are subject to change without prior notice.

FEATURE

1. High reliability and stability $\pm 1\%$
2. Sulfuration resistant against ASTM B-809 105°C 1000hrs
3. Automotive grade AEC Q-200 Compliant
4. 100% CCD inspection
5. RoHS 2 compliant with exemption 7C-I and Halogen free products

APPLICATION

- Automotive application
- Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The extra protective metal film is added onto top side electrodes to protect termination from sulfuration. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

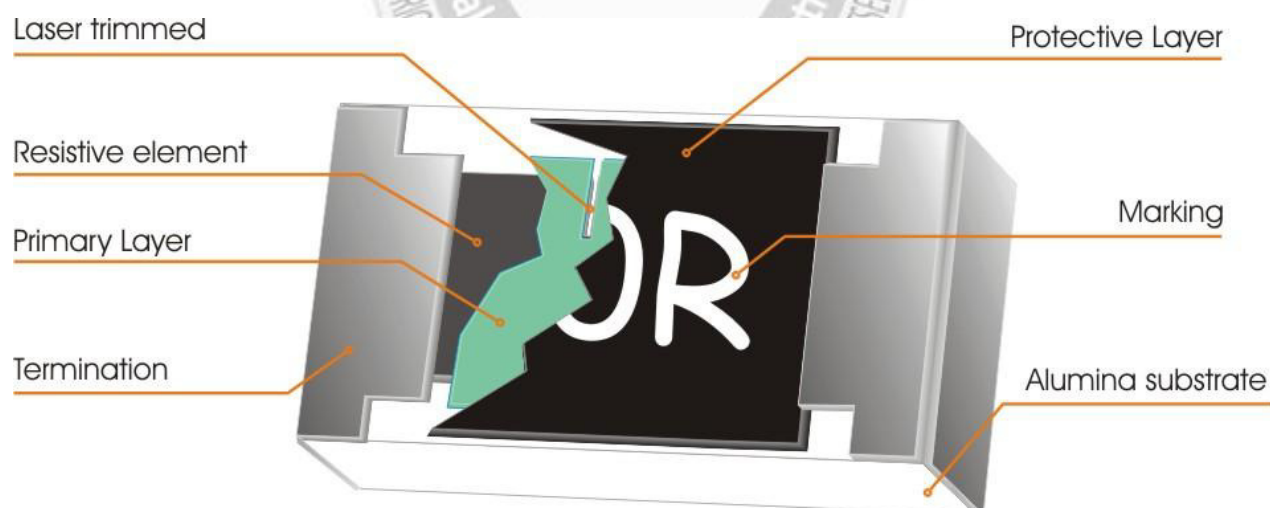


Fig 1. Construction of a Chip-R

QUICK REFERENCE DATA

Item	General Specification				
Series No.	SR10_W	SR12_W	SR08_W	SR06_W	SR04_W
Size code	1210(3225)	1206(3216)	0805(2012)	0603(1608)	0402(1005)
Resistance Range	1Ω~1MΩ				
Resistance Tolerance	± 1%, E96+E24 ± 5%, E24				
TCR (ppm/°C)					
10Ω < R ≤ 1MΩ	≤ ± 100	≤ ± 100	≤ ± 100	≤ ± 100	≤ ± 100
R ≤ 10Ω	-200~+400	-200~+400	-200~+400	-200~+400	-200~+400
Max. dissipation @ T _{amb} =70°C	1/3 W	1/4 W	1/8 W	1/10 W	1/10 W
Max. Operation Voltage (DC or RMS)	200V	200V	150V	75V	50V
Max. Overload Voltage (DC or RMS)	400V	400V	300V	150V	100V
Climatic category (IEC 60068)	55/155/56				

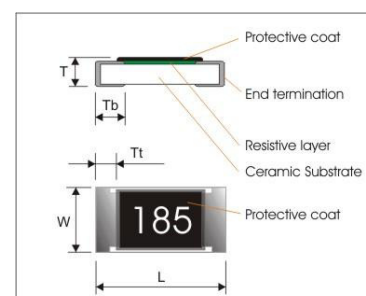
Note :

- This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}} \text{ or Max. RCWV listed above, whichever is lower.}$$

DIMENSIONS (unit : mm)

	SR10	SR12	SR08	SR06	SR04
L	3.10 ± 0.10	3.10 ± 0.10	2.00 ± 0.10	1.60 ± 0.10	1.00 ± 0.05
W	2.60 ± 0.10	1.60 ± 0.10	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
T	0.55 ± 0.10	0.60 ± 0.15	0.50 ± 0.15	0.45 ± 0.15	0.35 ± 0.05
Tb	0.50 ± 0.20	0.45 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.25 ± 0.10
Tt	0.50 ± 0.20	0.50 ± 0.20	0.40 ± 0.20	0.30 ± 0.10	0.20 ± 0.10



MARKING

Size \ Nr. Of digit of code\tolerance	$\pm 1\%, \pm 5\%$
1210/1206/0805	$\pm 1\%$: 4-digits marking; $\pm 5\%$: 3-digits marking
0603 (1608)	3-digits marking
0402(1005)	NO MARKING

3-digits marking (0603)

Nominal resistance				Description											
E-96 series				The 1st two digit codes are referring to the CODE on the table, the 3rd code is the index of resistance value :											
				$Y=10^{-2}$, $X=10^{-1}$, $A=10^0$, $B=10^1$, $C=10^2$, $D=10^3$, $E=10^4$, $F=10^5$											
				EX : 17.8Ω=25X , 178Ω=25A , 1K78 =25B 17K8=25C , 178K=25D , 1M78=25E											
3. Remark				There is no marking for the items are not under E-24 and E-96 series											
CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

4-digits marking (1210/1206/0805)

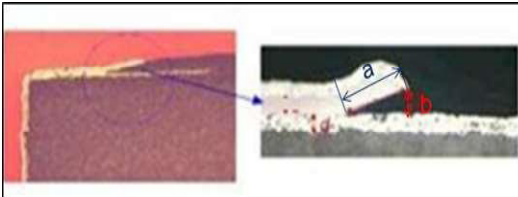
Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value.

Example

RESISTANCE	10 Ω	12 Ω	100 Ω	6800 Ω	47000 Ω
4-digits marking	10R0	12R0	1000	6801	4702

FUNCTIONAL DESCRIPTION

Product Dimension of a and b.

Location	Measuring point		Control Conditions
Extended length of end electrode	Measure the length of a dimension		Size above 0603: a>35um 0402: a >25um 0201: a> 15um
The thickness of the resistive protective layer by the electrode interface.	Measure the thickness of b dimension		Size above 0603: b>7um 0402/0201: b>6um

Product characterization

Standard values of nominal resistance are taken from the E24& E96 series for resistors with a tolerance of $\pm 0.5\%$, $\pm 0.1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063"

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

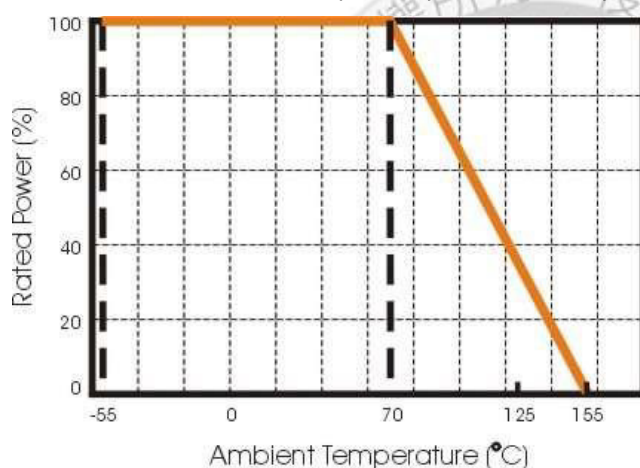


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

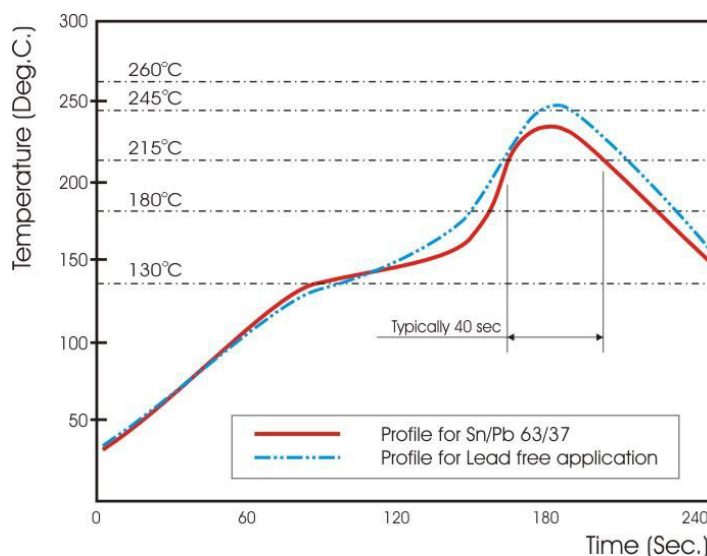


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

SR12	X	1020	F	T	L	W
Automotive code	Type code	Resistance code	Tolerance	Packaging code	Termination code	Special code
SR10 : 1210	X : ±1%, 10-1M	E24+E96: 3 significant digits followed by no. of zeros	F : ±1%	T : 7" Reeled taping	L = Sn base (lead free)	W = Anti-sulfur ASTM B-809 105°C 1000hrs
SR12 : 1206	±5%, 1-10M	E24: 102Ω = 1020	J : ±5%	Q : 10" Reeled taping		
SR08 : 0805	W : ±1%, <10, >1M	E96: 37.4KΩ = 3742	P : Jumper	G : 13" Reeled taping 70K/RL		
SR06 : 0603				H : 13" Reeled taping 50K/RL		
SR04: 0402				B : Bulk		

SR10, SR12, SR08, SR06:

1. Reeled tape packaging : 8mm width paper taping 5000pcs per 7" reel, 10kpcs per 10" reel, 20kpcs per 13" reel.
2. Bulk packaging : 5000pcs per poly-bag

SR04:

1. Reeled tape packaging : 8mm width paper taping 10,000pcs per reel,
2. Bulk packaging : 10,000pcs per poly-bag

TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category **LCT/UCT/56**(rated temperature range : **Lower Category Temperature, Upper Category Temperature**; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, sub-clause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

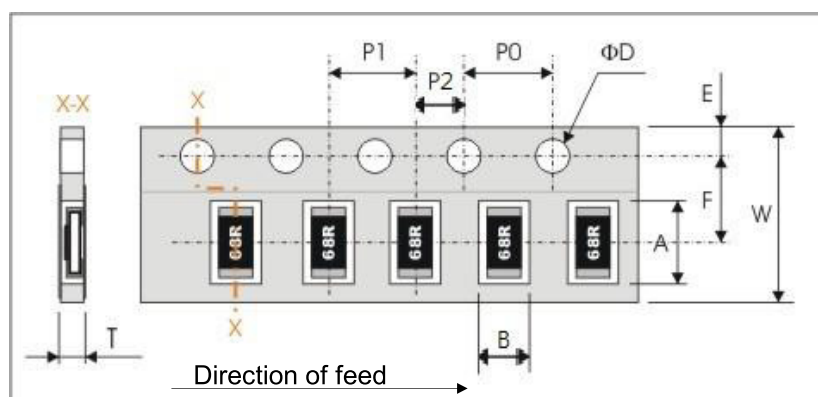
All soldering tests are performed with mildly activated flux.

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} \quad t_1 : 20^\circ\text{C} + 5^\circ\text{C} - 1^\circ\text{C}$ R ₁ : Resistance at reference temperature R ₂ : Resistance at test temperature	Within the specified tolerance Refer to "QUICK REFERENCE DATA"	
Resistance to soldering heat (R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C±5°C	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Solderability J-STD-002	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C / 5sec. b) Steam the sample dwell time 1 hour/ solder dipping 215°C / 5sec. c) Steam the sample dwell time 1 hour/ solder dipping 260°C / 7sec.	95% coverage min., good tinning No visible damage	
Temperature cycling JESD22 method JA-104	1000 cycles, -55°C ~ +155°C, dwell time 5~10min	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
High Temperature Exposure MIL-STD-202 method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Mechanical Shock MIL-STD-202 method 213	1/2 Sine Pulse / 1500g Peak / Velocity 15.4ft/sec	Within the specified tolerance No visible damage	<50mΩ
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending once 2mm for 10sec	ΔR/R max. ±(1.0%+0.05Ω). No visible damage	<50mΩ
Terminal strength AEC-Q200-006	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations	
Vibration MIL-STD-202 method 204	Test 5g's for 20min., 12 cycles each of 3 orientations	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Thermal shock MIL-STD-202 method 107	Test -55 to 155°C / dwell time 15min/ Max transfer time 20sec 300cycles	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
ESD AEC-Q200-002	Test contact 1.0KV (0.5KV for 0402 only)	ΔR/R max. ±(1.0%+0.05Ω) No visible damage	<50mΩ
Sulfuration test ASTM B-809	105°C 1000hrs against EIA-977-2016 condition B	ΔR/R max. ±(2.0%+0.05Ω) No visible damage	<50mΩ

PACKAGING

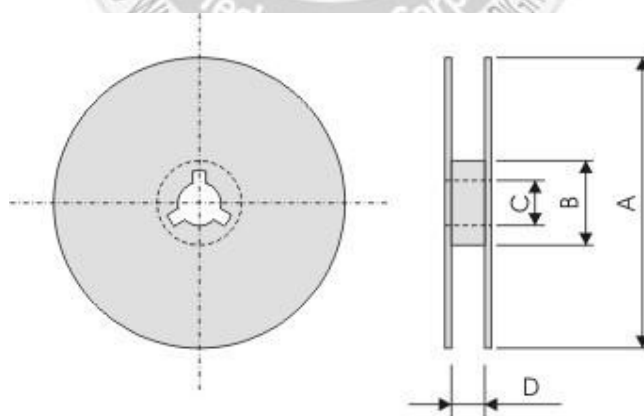
Paper Tape specifications (unit :mm)



Series No.	A	B	W	F	E
SR10	3.60±0.20	3.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10
SR12	3.60±0.20	2.00±0.20			
SR08	2.40±0.20	1.65±0.20			
SR06	1.90±0.20	1.10±0.20			
SR04	1.20±0.10	0.70±0.10			

Series No.	P1	P0	P2	ΦD	T
SR10/12/08	4.00±0.10	4.00±0.10	P2=1/2P0	Φ1.50 ^{+0.1} _{-0.0}	0.80±0.1
SR06					0.70±0.05
SR04					0.50±0.05

Reel dimensions (unit :mm)



Symbol	A	B	C	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5
10" reel	Φ254.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5
13" reel	Φ330.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5